



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/788,219

Filed: February 16, 2001

Inventor(s):

Jeffrey L. Kodosky, Hugo  
Andrade, Brian Keith Odom  
and Cary Paul Butler

Title: System and Method for  
Converting a Graphical  
Program Including a  
Structure Node into a  
Hardware Implementation

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Examiner: Frejd, Russell  
Warren  
Group/Art Unit: 2128  
Atty. Dkt. No: 5150-23006

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

Name of Registered Representative

*Jeffrey C. Hood*

Signature

September 27, 2004

Date

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☐ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;  
☐ before the mailing date of a first Office Action on the merits; or  
☒ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed below.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed below.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☐ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

4. For each non-English language reference listed on the attached Form PTO-1449:

- ☒ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this

Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

Applicant does not believe that any fees are required relating to this Information Disclosure Statement. The Commissioner is hereby authorized to charge any fees which may be required or credit any overpayment to Meyertons, Hood, Kivlin, Kowert & Goetzel PC, Deposit Account No. 50-1505/5150-23006/JCH.


Respectfully submitted,



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Date: September 27, 2004

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-23006		SERIAL NO: 09/788,219			
							
		APPLICANT: Kodosky et al.					
FILING DATE: February 16, 2001		GROUP: 2128					
<b>U.S. PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	C1	4,901,221	Feb. 13, 1990	Kodosky, et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
	C2	DE 692 32 869 T2	Sept. 4, 2003	Germany			YES
	C3	DE 42 05 524 A1	Aug. 27, 1992	Germany			YES
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	C4	Levy, M., "DSP Design Tools Target FPGAs," <a href="http://www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm">www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm</a> , June 20, 1996.					
	C5	Heimdahl, M.P.E., Keenan, D.J., "Generating Code from Hierarchical State-Based Requirements," Proceedings of IEEE International Symposium on Requirements Engineering (RE'97)," January 1997.					
	C6	Integrated Systems, Inc., "MATRIXx Product Family Technical Specifications," Product Manual, Copyright 1995.					
	C7	Pauer, E.K., "Multiprocessor System Development for High Performance Signal Processing Applications," Proceedings of the 1997 IEEE Int. Workshop on Rapid System Prototyping (RSP)," Copyright 1997.					
	C8	Drusinsky, D., "Extended State Diagrams and Reactive Systems," Dr. Dobb's Journal, October 1994.					
	C9	Ade, M., Lauwereins, R., and Peperstraete, J.A., "Hardware-Software Codesign with GRAPE," Rapid System Prototyping, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping (RSP'95), 1995.					
	C10	Chen, X., Ling, X., Amano, H., "Software Environment for WASMII: a Data Driven Machine with a Virtual Hardware," Proceedings of 4 <sup>th</sup> Workshop on Field-Programmable Logic and Applications (FPL '94), pages 208-219, Springer Verlag, September 1994.					
EXAMINER:				DATE CONSIDERED:			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.							